Title: METHOD AND APPARATUS FOR CONNECTING A MASSIVELY PARALLEL PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT



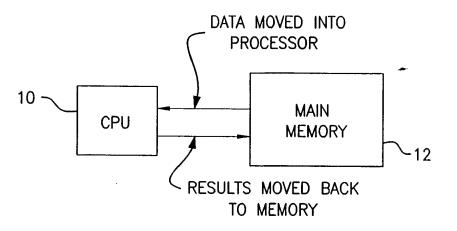


FIG. 1 PRIOR ART

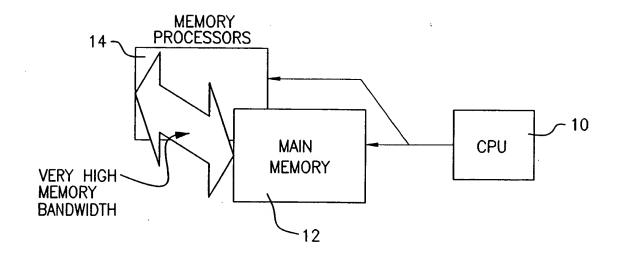
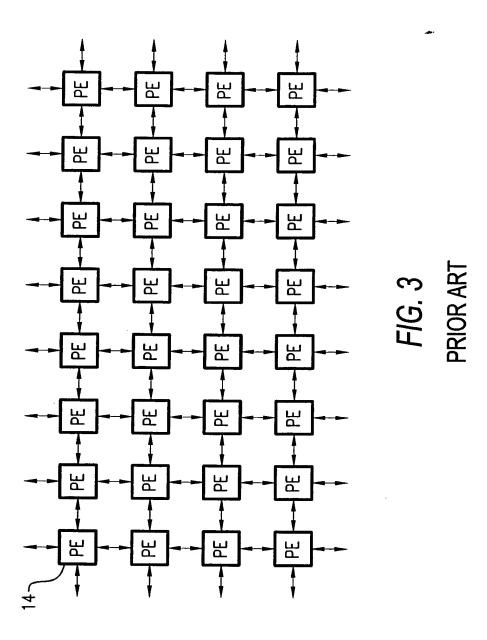


FIG. 2 PRIOR ART

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SERIAL MANNER
REPLACEMENT SHEET

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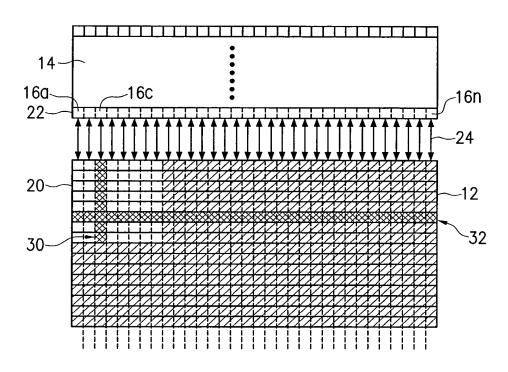
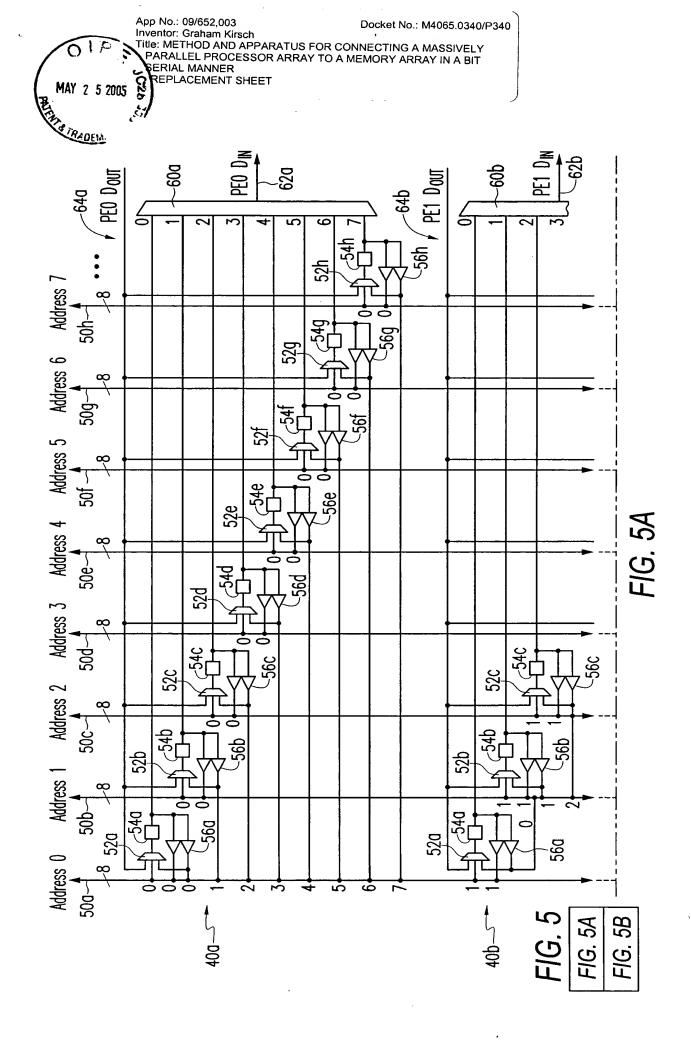


FIG. 4

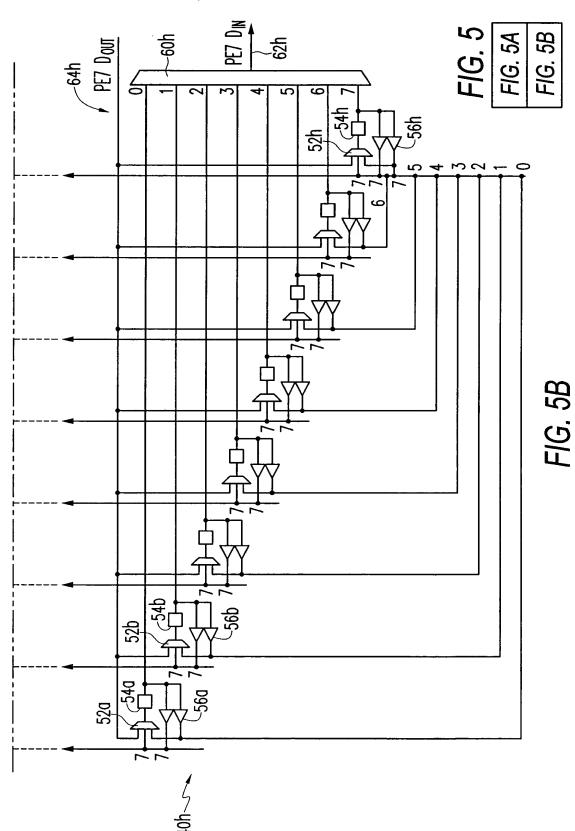


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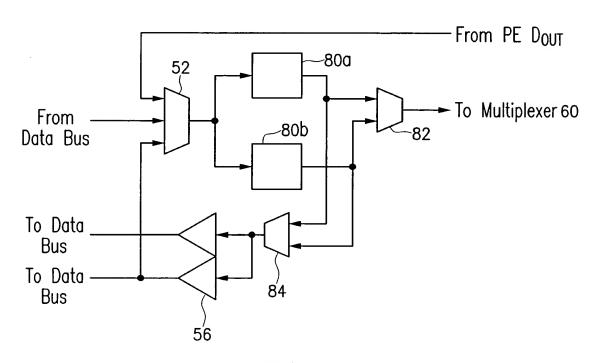


FIG. 6

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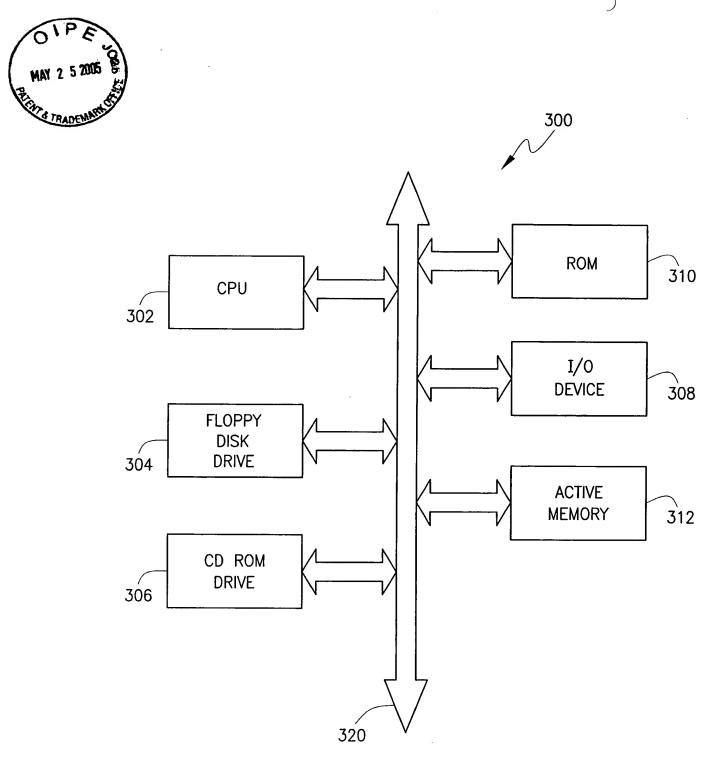


FIG. 7